Universal PCI Multi-IO Communication Board

User’s Manual

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Safety Information
2. Always read the safety information carefully.
3. Keep this equipment away from direct sunlight, or in humid or damp places.
4. Do not place this equipment in an unstable position, or on vibrating surface before setting it up.
5. Do not use or place this equipment near magnetic fields, televisions, or radios to avoid electronic interface that affects device performance.
Regulatory Compliance

FCC Conditions

This equipment has been tested and found to comply with Part 15 of the FCC Rules. Operation is subject to the following two conditions:

(1) This equipment may not cause harmful interference
(2) This equipment must accept any interference received, including interference that may cause undesired operation.

Important! Changes or modifications not expressly approved by the manufacturer responsible for compliance could void the user’s authority to operate the equipment. Use an approved phone set.

CE

This equipment is in compliance with the requirements of the following regulations: EN 55022:
CLASS B

WEEE Information

For EU (European Union) member users: According to the WEEE (Waste electrical and electronic equipment) Directive, do not dispose of this product as household waste or commercial waste. Waste electrical and electronic equipment should be appropriately collected and recycled as required by practices established for your country. For information on recycling of this product, please contact your local authorities, your household waste disposal service or the shop where you purchased the product.
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WHQL Certification Approval

The Designed for Microsoft Windows 32/64-bit operation system WHQL logo identifies products that meet Microsoft’s quality standards, SUNIX I/O products carry with this logo and listed on Windows Catalog. WHQL logo includes below operation system version

Microsoft Windows Client: Windows XP / Vista / 7 / 8 / 8.1 (X86/X64)
Microsoft Windows Server: Windows 2003 / 2008 / 2012 (X64)
1. Introduction

SUNIX Golden I/O series, a line of Universal PCI Multi-port Serial and Parallel Multi-IO Communication Board, is designed for 3.3V / 5V and 32 / 64-bit PCI Bus with Plug and Play feature. It can be installed in virtually any available PC system and compatible with all major operating systems. Users do not need to manually set jumpers to configure I/O addresses and IRQ locations.

This board offers independent both serial and parallel ports for connecting terminals, modems, printers, scanners, cash registers, bar code readers, keypads, numeric displays, electrical scales, data acquisition equipment, and other serial / parallel devices for the PC and compatible systems. This board offers a reliable and high performance solution for Multi-IO communications.

The following topics covered in this chapter:

◆ 1.1 Overview
◆ 1.2 Package Checklist
◆ 1.3 Product Features
◆ 1.4 Product Specifications
1.1 Overview

The majority of today’s motherboards are not equipped with any RS-232 or Parallel port. With SUNIX Universal PCI Serial and Parallel Multi-IO Communication Board, users can expand both COM and LPT ports on their system, allowing them to use serial and parallel devices further. It is compatible with RS-232.V24 standard serial and IEEE1284 Parallel interfaces. Furthermore, this Multi-IO board supports majority of the OS on the market. We provide driver support for Microsoft Windows and Linux 32/64-bit platform, making it the best and cost efficient solution for commercial and industrial automation applications.

1.2 Package Checklist

Please check if the following items are present and in good condition upon opening your package. Contact your vendor if any item is damaged or missing.

1. Hardware:
   Universal PCI Serial and Parallel Multi-IO Communication Board, × 1

   Cable: (Only for 2S/1P, 2S/2P or 4S/1P Solution)
   13x2 Pin Header to DB25F Flat Cable With Bracket set × 1

2. CD Driver

3. User's Manual (This document)
1.3 Product Features

- Expands both RS-232 COM and IEEE1284 LPT ports on system.
- High performance SUNIX 16C950 UART Multi-IO controller on-board.
- Ultra low power consumption design for Green Environment.
- Compliance with PCI 33MHz Version 3.0/2.3/2.2./2.1 specification.
- Supports both 64-bit PCI-X & 32-bit PCI bus slot.
- RS-232 COM port communication speed supports up to 115.2Kbps (921.6Kbps Optional*).
- On-chip hardware auto flow control to guarantee no data loss.
- Built-in ±2KV ESD protection for all serial signals (± 15KV ESD Optional*).
- Parallel LPT port supports IEEE 1284-1994 parallel port standard
  - ECP(Enhance Capacity Port) / EPP(Enhance Parallel Port)
  - SPP(Standard Parallel Port) / BPP(Bi-direction Parallel Port)
- Auto-switching between ECP/EPP/SPP/BPP modes without configuration.
- Plug-n-Play, I/O address and IRQ assigned by system.
- Certified by CE, FCC, RoHS, and Microsoft WHQL approval.
- Support Microsoft Windows, Linux, and DOS.
## 1.4 Product Specifications

### Board Communication

<table>
<thead>
<tr>
<th>Interface</th>
<th>Universal PCI 64/32bit</th>
</tr>
</thead>
<tbody>
<tr>
<td>BUS</td>
<td>PCI Spec.Ver 3.0/2.3/2.2/2.1</td>
</tr>
<tr>
<td>Controller</td>
<td>SUNIX SUN1989 (16C950 UART Compatible)</td>
</tr>
<tr>
<td>IRQ &amp; IO</td>
<td>Assigned by System</td>
</tr>
</tbody>
</table>

### Serial Communication

<table>
<thead>
<tr>
<th>Interface</th>
<th>RS-232</th>
</tr>
</thead>
<tbody>
<tr>
<td>No. of Port</td>
<td>1, 2, or 4-port (Depend on product user bought)</td>
</tr>
<tr>
<td>FIFO</td>
<td>128 byte Hardware</td>
</tr>
<tr>
<td>Signal</td>
<td>TxD, RxD, RTS, CTS, DTR, DSR, DCD, RI, GND</td>
</tr>
<tr>
<td>Baud rate</td>
<td>50bps ~115.2Kbps (921.6Kbps Optional*)</td>
</tr>
<tr>
<td>Stop bit</td>
<td>1, 1.5, 2</td>
</tr>
<tr>
<td>Parity</td>
<td>even, odd, none, mark, space</td>
</tr>
<tr>
<td>Flow Control</td>
<td>None, Xon/Xoff, RTS/CTS</td>
</tr>
<tr>
<td>Protection</td>
<td>±2KV ESD protection for each signal Human Body Model (HBM) (±15KV ESD Optional*)</td>
</tr>
<tr>
<td>Board Connector</td>
<td>DB9 Male</td>
</tr>
</tbody>
</table>

### Parallel Communication

<table>
<thead>
<tr>
<th>Interface</th>
<th>IEEE-1284</th>
</tr>
</thead>
<tbody>
<tr>
<td>Mode</td>
<td>SPP/ECP/EPP/BPP (Auto-switching)</td>
</tr>
<tr>
<td>Data Speed</td>
<td>Maximum 1.8 MB/s</td>
</tr>
<tr>
<td>FIFO</td>
<td>16 byte Hardware</td>
</tr>
<tr>
<td>Number of Port</td>
<td>1 or 2-port (Depend on product user bought)</td>
</tr>
<tr>
<td>Board Connector</td>
<td>DB25 Female</td>
</tr>
</tbody>
</table>

### Driver Support

| Windows Client | XP / Vista / 7 / 8 / 8.1 (X86/X64) |
| Windows Server | 2003 / 2008 / 2012 (X64)          |
| Microsoft Embedded | Windows XP Embedded / POS Ready 2009 / Embedded System |
| Linux          | Linux 2.4.x / 2.6.x             |
| DOS            | DOS                           |
Regulatory Approvals

<table>
<thead>
<tr>
<th>Hardware</th>
<th>EN55022 Class B, EN55024, EN61000-3-2, EN61000-3-3, FCC Part 15 Class B, RoHS</th>
</tr>
</thead>
<tbody>
<tr>
<td>Software</td>
<td>Microsoft WHQL Windows&lt;br&gt;Microsoft Client: XP / Vista / 7 / 8 / 8.1 (X86/X64)&lt;br&gt;Microsoft Server: 2003 / 2008 / 2012 (X64)</td>
</tr>
</tbody>
</table>

Environment

<table>
<thead>
<tr>
<th>Operation Temperature</th>
<th>0 to 60°C (32 to 140°F)</th>
</tr>
</thead>
<tbody>
<tr>
<td>Operation Humidity</td>
<td>5 to 95% RH</td>
</tr>
<tr>
<td>Storage Temperature</td>
<td>-20 to 85°C (-4 to 185°F)</td>
</tr>
</tbody>
</table>

**Note:**
- SUNIX High Speed RS-232 Card (H Version) supports ±15KV ESD protection for each signal and 921.6Kpbs baud rate setting. Please refer to the Chapter 5 Appendix, Product Family for detail.

- LPT port does NOT support legacy 278 / 378 ISA address under Windows nor Linux operation systems, but only DOS mode.

- Do not support LPT Key Locker Parallel devices, because of legacy ISA address limitation.
This chapter includes information about hardware installation for Universal PCI Serial and Parallel Multi-IO Communication Board. The following topics are covered:

◆ 2.1 Hardware Installation
◆ 2.2 Pin Assignments
2.1 Hardware Installation

The hardware installation of Universal PCI Serial and Parallel Multi-IO Communication Board is easy to carry out. Before inserting the card into the PCI bus, please follow the detailed steps given below to install the PCI board in your computer.

Safety First
To avoid damaging your system and boards, make sure your PC’s power is turned off before installing PCI card.

Step 1: Turn your PC’s power off, and shut off the power to any peripheral.
Step 2: Remove the power plug from the plug socket.
Step 3: Remove the cover from the computer case.
Step 4: If fitted. Remove the metal cover plate on the rear of a free PCI slot.
Step 5: Insert Universal PCI Multi-Port Communication Board into the free PCI slot and screw it firmly on the bracket side.
Step 6: Place the cover back onto the computer.
Step 7: Insert the plug into the plug socket.
For 2S/1P or 4S/1P PCI Multi-IO Card, there is one extend flat cable along with the board. Please connect the cable 2x13 female header to the board. The red wire matches with the arrow mark (first pin) on the board.

Fix the bracket on the PC chassis.
2.2 Pin Assignment

This chapter provides the pin assignments for SUNIX Universal PCI Serial and Parallel Multi-IO Communication Board, as well as the pin assignments for the optional accessories.

RS-232 Interface:

<table>
<thead>
<tr>
<th>PIN</th>
<th>DB9M</th>
<th>DB25M</th>
<th>RJ45</th>
<th>Pin Header</th>
</tr>
</thead>
<tbody>
<tr>
<td>DCD</td>
<td>1</td>
<td>8</td>
<td>7</td>
<td>1</td>
</tr>
<tr>
<td>RxD</td>
<td>2</td>
<td>3</td>
<td>6</td>
<td>3</td>
</tr>
<tr>
<td>TxD</td>
<td>3</td>
<td>2</td>
<td>3</td>
<td>5</td>
</tr>
<tr>
<td>DTR</td>
<td>4</td>
<td>20</td>
<td>2</td>
<td>7</td>
</tr>
<tr>
<td>GND</td>
<td>5</td>
<td>7</td>
<td>4</td>
<td>9</td>
</tr>
<tr>
<td>DSR</td>
<td>6</td>
<td>6</td>
<td>8</td>
<td>2</td>
</tr>
<tr>
<td>RTS</td>
<td>7</td>
<td>4</td>
<td>1</td>
<td>4</td>
</tr>
<tr>
<td>CTS</td>
<td>8</td>
<td>5</td>
<td>5</td>
<td>6</td>
</tr>
<tr>
<td>RI</td>
<td>9</td>
<td>22</td>
<td>-</td>
<td>8</td>
</tr>
</tbody>
</table>
## Parallel Interface:

![Diagram of DB25F connector]

<table>
<thead>
<tr>
<th>DB25F</th>
<th>PIN</th>
<th>DB25F</th>
<th>PIN</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>STROBE</td>
<td>14</td>
<td>AUTO FEED</td>
</tr>
<tr>
<td>2</td>
<td>DATA0</td>
<td>15</td>
<td>ERROR</td>
</tr>
<tr>
<td>3</td>
<td>DATA1</td>
<td>16</td>
<td>INT</td>
</tr>
<tr>
<td>4</td>
<td>DATA2</td>
<td>17</td>
<td>SELECT INPUT</td>
</tr>
<tr>
<td>5</td>
<td>DATA3</td>
<td>18</td>
<td>GND</td>
</tr>
<tr>
<td>6</td>
<td>DATA4</td>
<td>19</td>
<td>GND</td>
</tr>
<tr>
<td>7</td>
<td>DATA5</td>
<td>20</td>
<td>GND</td>
</tr>
<tr>
<td>8</td>
<td>DATA6</td>
<td>21</td>
<td>GND</td>
</tr>
<tr>
<td>9</td>
<td>DATA7</td>
<td>22</td>
<td>GND</td>
</tr>
<tr>
<td>10</td>
<td>ACKNOWLEDGE</td>
<td>23</td>
<td>GND</td>
</tr>
<tr>
<td>11</td>
<td>BUSY</td>
<td>24</td>
<td>GND</td>
</tr>
<tr>
<td>12</td>
<td>PAPER EMPTY</td>
<td>25</td>
<td>GND</td>
</tr>
<tr>
<td>13</td>
<td>SELECT</td>
<td></td>
<td></td>
</tr>
</tbody>
</table>
After installing the Universal PCI Serial and Parallel Multi-IO Communication Board in your system successfully, please follow the step by step software installation guide to confirm how to install appropriate driver and configure the serial port settings.

The driver for PCI board supports Windows and Linux operating systems, and you can select your requirement in the following chapter:

The following topics covered in this chapter:

- 3.1 Windows Driver Install
- 3.2 Windows Driver Uninstall
- 3.3 Windows Verify Installation
- 3.4 Linux Driver Installation
3.1 Windows Driver Installation

Please refer to following instructions to install the driver for the first time under Windows operation system. You will need to plug the board in an available PCI or PCI-X slot first, before installing the driver.

(1) After the board is physically installed and the PC boots up, system will detect the PCI Multi-IO card and prompt for driver installation wizard, please choose cancel.

(2) Put CD driver bound with product in your CD / DVD ROM drive. Please select “Run autorun.exe”, and then select “Driver Installation”.

![Image 1](image1.png)

![Image 2](image2.png)
(3) Please select the product interface you bought, such as PCI.

(4) Please select the O.S. version you are using, such as Windows Vista. Then system will process the driver installation step automatically.
(5) Click "**Next**" to continue driver installation steps.

(6) Click "**Finish**" to end installation steps. If SUNIX I/O card install correct in your system, you can read "✓" icon in this picture.
3.2 Windows Driver Uninstall

Please refer to following instructions uninstall Multi-I/O card driver.

(1) Click on the “Programs and Features” tab in the Windows Control Panel.

Start > Controller Panel > Programs and Features

(2) Entry Uninstall or change a program page, and double click “Windows Driver Package – SUNIX Co., Ltd SUNIX Multi-I/O Controller” to process driver uninstall procedure.
3.3 Windows Verify Installation

You can use Windows “Device Manager” to verify proper installation.

(1) Click on the “Programs and Features” tab in the Windows Control Panel.

Start > Controller Panel > Device Manager

(2) In the Device Manager window, you should see this board under Multifunction adapters (SUNIX 1-port RS-232 Serial Card / 1-port Parallel Multi-IO Card in this example). You should also see SUNIX COM and LPT port under Ports (COM & LPT).
3.4 Linux Driver Installation

This installation guide describes the procedures to install the PCI Multi-IO board in Linux kernel 2.4.x and 2.6.x. Please refer to “snx_Vx.x.x.x.tar.gz” for driver installation detail in CD Driver (Linux folder) directory.

(1) Driver install

Please create a directory under root directory, e.g /temp, do commands:

```
# cd /
# mkdir temp
```

After get driver file "snx_Vx.x.x.x.tar.gz". Copy file to /temp directory, then extract and install, do commands:

```
# cp snx_Vx.x.x.x.tar.gz /temp
# cd /temp
# tar xvfz snx_Vx.x.x.x.tar.gz
# cd /temp/snx
# make clean ; make install
```

********************************************************************************

* If system is Suse 9.0 and errors occur when
* "make clean ; make install", do commands:
*
* # cd /usr/src/linux/
* # make cloneconfig
* # make dep
*
* then do "make clean ; make install" again in /temp/snx
********************************************************************************
Load driver module, do command:

```
# modprobe snx
or
# insmod /temp/snx/driver/snx.ko (snx.o for kernel 2.4)
```

Check driver module, do command:
```
# lsmod | grep snx
```

Unload driver, do command:
```
# rmmod snx
```

(2) Device node creation

Each serial port has one device node which is named "ttySNX?", maximum up to 32 serial ports.

Each parallel port has two device node which is name "lp?" and "parport?". This step will backup lp2~lp3 and parport2~parport3 to lp?.bak and parport?.bak in /dev for your system first. Then, create lp2~lp3 and parport2~parport3 in /dev for sunix driver, maximum up tp 2 parallel ports.

This setup will be done when do "make clean ; make install", if device nodes aren't in /dev, do commands:

```
# cd /temp/snx/snxmknod
# ./snxmknod
```

This will create device nodes in /dev.

If there are more than two boards installed, serial port device naming convention please refer to F1.
4. Port Configuration

This chapter shows all Serial COM and Parallel LPT port settings that user came with usually, such as port number, FIFO length(size), baud rate, IO address and others.

The following topics covered in this chapter:

◆ 4.1 Configure COM / LPT Port Settings
◆ 4.2 COM / LPT Port Number Settings
◆ 4.3 COM / LPT System Resource
◆ 4.4 COM Port FIFO Settings
4.1 Configure COM / LPT Port Settings

After the board drivers are installed, please refer to following instructions to configure Serial COM and Parallel LPT port settings.

(1) Please launch the "Device Manager".
(2) Right click the "SUNIX 2-port RS-232 Serial Card / 1-port Parallel Multi-IO Card" item from the "Multifunction adapters" sub-tree and click "Properties".

(3) On the "Port Control" tab, select a port to configure. You can set both COM and LPT ports settings through this page.
   * Click "OK" to approve the settings for the selected port.
   * Click "Apply to All" to approve the settings for all COM and LPT ports.
4.2 COM / LPT Port Number Settings

Under Port Number, select a COM number to assign to the serial port. Click “OK” to approve the settings for the selected port.

Note: In order to prevent system resource conflict, do not select “in use” port.

4.3 COM / LPT System Resource

User can read the COM or LPT port’s “IO Range” and “IRQ” located in system by selecting assigned port.

4.4 COM Port FIFO Settings

Select RS-232 COM port’s Receiver FIFO Trigger and Transmit FIFO Size. The default Rx FIFO Trigger is 112 bytes. The default Tx FIFO Size is 128 bytes. Click “Apply to All” to change this setting for all serial ports on the board. Then click “OK” to save the settings. If the system is not stable, select the lower value to correct problems.
This chapter shows some problems that user came with usually. Also you can check it if the PCI serial and parallel board can not work properly in your system after following hardware and software installation steps. In addition, you could contact with us for detail technical product information.

In this appendix, we cover the following topics.

- **5.1 Troubleshooting**
- **5.2 Product Family**
- **5.3 Contact Information**
5.1 Troubleshooting

1. System fails to find the PCI serial & parallel board or COM / LPT port.
   A: It may cause by following issue:
   a. The board is not properly plugged into the PCI slot.
   b. Please clean the golden finger.
   c. The PCI slot is defective. Please try other slots until you find one that
      works.
   d. The mainboard does not have an available IRQ for the PCI serial board.
      Enter the PC’s BIOS and make sure an IRQ setting is available in the
      PCI/PnP settings.
   e. The board itself might be defective. You can try another mainboard
      testing this board working or not.

2. There is a blue screen when I entry operation system.
   A: The possible reason is an IRQ or I/O address conflict with other PCI bus
   adapters, such as LAN or serial boards, or with the system BIOS. Refer to
   the corresponding problem in the previous FAQ for solutions.

3. There are some exclamation marks in device manager and serial ports
   can not work properly.
   A: It caused by the wrong driver installing or hardware settings. Please turn off
   your computer firstly and re-install hardware and software, especially
   re-install the correct driver. If problem still happens, please change another
   PCI Bus slot to install this board.

4. How can I set the LPT port to the legacy 278 or 378 ISA address?
   A: Because of PCI plug-n-play rule and windows operation system limitation,
   you can NOT remap to 278H or 378H legacy ISA IO address under
   Microsoft Windows 2000, XP, 2003 Vista, 2008, 7 or Linux OS.
5. How large FIFO length should I set?
A: FIFO (First-in-First-out) buffers are used to reduce the frequency of interrupt processes for UART chips. The size of the buffer will determine the number of times the cards need to interrupt the computer’s CPU in order to process a string of data. With larger FIFO buffer size; there is more data flow and less interruption to the CPU, therefore allowing the CPU to be free to handle other more crucial tasks.

Set the Receive/Transmit Buffer to higher value will get faster performance because the interrupts will be reduced, but the time for interrupt service routine will become shorter. The receive buffer overflow will be easily happened if the CPU speed is not enough to handle. If the system is not stable, select the lower value to correct problems.

6. How come my parallel device can not work on this Card, but work properly under on-board LPT port??
A: a. Please confirm your parallel device connect to the LPT port correctly.
   b. Make sure the LPT port number you connected matches with parallel device settings.
   c. Your parallel device only works under 278/378 legacy ISA IO address. This card can not satisfy with this feature. Please contact with your parallel device vendor for driver updating.

7. How can I set the different ECP / EPP / SPP / BPP operation mode?
A: Under Windows OS such as 2000, XP, Vista, and 7, PCI parallel card will automatically communicate with the device to which it is connected and sets to that particular mode. For example if this card is connected to a printer that support SPP mode, then this parallel card will communicate with this printer and will automatically set to SPP mode. It means that this card will handshake with the device to which it is connected and configures to that mode. User does not require changing to any particular mode. This parallel card’s working mode can not force setting particular mode by driver or BIOS!!
5.2 Product Family

SUNIX provides kinds of RS-232 and IEE1284 interface cards for customer selection, including PCI Express, PCI, CardBus, and ExpressCard. Please refer to the product family table for reference.

### Multi I/O PCI Express Interface

<table>
<thead>
<tr>
<th>Port</th>
<th>Connector</th>
<th>Serial Baud Rate</th>
<th>Power output</th>
<th>Bracket</th>
<th>Model NO.</th>
</tr>
</thead>
<tbody>
<tr>
<td>COM</td>
<td>LPT</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>DB9 Male</td>
<td>115.2Kbps</td>
<td>Standard</td>
<td>MIO6469A</td>
<td></td>
</tr>
<tr>
<td></td>
<td>DB25 Female</td>
<td>921.6Kbps</td>
<td></td>
<td>MIO6469H</td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>DB9 Male</td>
<td>115.2Kbps</td>
<td>Low Profile</td>
<td>MIO6479AL</td>
<td></td>
</tr>
<tr>
<td></td>
<td>DB25 Female</td>
<td>921.6Kbps</td>
<td></td>
<td>MIO6479H</td>
<td></td>
</tr>
<tr>
<td></td>
<td>DB44 Female</td>
<td>115.2Kbps</td>
<td></td>
<td>MIO6479AL</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>921.6Kbps</td>
<td></td>
<td>MIO6479H</td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>DB44 Female</td>
<td>115.2Kbps</td>
<td>Standard</td>
<td>MIO5499A</td>
<td></td>
</tr>
<tr>
<td></td>
<td>13x2 Pin Header</td>
<td>921.6Kbps</td>
<td></td>
<td>MIO5499H</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>921.6Kbps</td>
<td>Low Profile</td>
<td>MIO5499AL</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>921.6Kbps</td>
<td></td>
<td>MIO5499H</td>
<td></td>
</tr>
</tbody>
</table>

### Multi I/O PCI Interface

<table>
<thead>
<tr>
<th>Port</th>
<th>Connector</th>
<th>Serial Baud Rate</th>
<th>Power output</th>
<th>Bracket</th>
<th>Model NO.</th>
</tr>
</thead>
<tbody>
<tr>
<td>COM</td>
<td>LPT</td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>1</td>
<td>DB9 Male</td>
<td>115.2Kbps</td>
<td>5V</td>
<td>Standard</td>
<td>MIO5069A</td>
</tr>
<tr>
<td></td>
<td>DB25 Female</td>
<td>921.6Kbps</td>
<td>12V</td>
<td>MIO5069P</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>-</td>
<td></td>
<td>MIO5069H</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>5V/12V</td>
<td></td>
<td>MIO5069PH</td>
<td></td>
</tr>
<tr>
<td>2</td>
<td>5x2 Pin Header</td>
<td>115.2Kbps</td>
<td>Standard</td>
<td>MIO5069U</td>
<td></td>
</tr>
<tr>
<td></td>
<td>13x2 Pin Header</td>
<td>921.6Kbps</td>
<td></td>
<td>MIO5069UL</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>-</td>
<td>Low Profile</td>
<td>MIO5069UL</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>921.6Kbps</td>
<td>Standard</td>
<td>MIO5069UH</td>
<td></td>
</tr>
</tbody>
</table>
## Multi I/O PCI Interface

<table>
<thead>
<tr>
<th>Port</th>
<th>COM</th>
<th>LPT</th>
<th>COM</th>
<th>LPT</th>
<th>Serial Baud Rate</th>
<th>Power output</th>
<th>Bracket</th>
<th>Model NO.</th>
</tr>
</thead>
<tbody>
<tr>
<td>2</td>
<td>1</td>
<td></td>
<td>DB9 Male</td>
<td>13x2 Pin Header</td>
<td>115.2Kbps</td>
<td>Standard</td>
<td>MIO5079A</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>921.6Kbps</td>
<td></td>
<td>MIO5079H</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>5x2 Pin Header</td>
<td>13x2 Pin Header</td>
<td>115.2Kbps</td>
<td>Low Profile</td>
<td>MIO5079UL</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>921.6Kbps</td>
<td></td>
<td>MIO5079UH</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td>DB44 Female</td>
<td></td>
<td>115.2Kbps</td>
<td>Low Profile</td>
<td>MIO5079AL</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>921.6Kbps</td>
<td></td>
<td>MIO5079HL</td>
<td></td>
</tr>
<tr>
<td>4</td>
<td>1</td>
<td></td>
<td>DB44 Female</td>
<td>13x2 Pin Header</td>
<td>115.2Kbps</td>
<td>Standard</td>
<td>MIO5099A</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>921.6Kbps</td>
<td>Low Profile</td>
<td>MIO5099H</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>MIO5099AL</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>MIO5099HL</td>
<td></td>
</tr>
</tbody>
</table>

## Multi I/O ExpressCard Interface

<table>
<thead>
<tr>
<th>Port</th>
<th>COM</th>
<th>LPT</th>
<th>COM</th>
<th>LPT</th>
<th>Serial Baud Rate</th>
<th>Power output</th>
<th>Bracket</th>
<th>Model NO.</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>1</td>
<td></td>
<td>DB44 Female</td>
<td></td>
<td>921.6Kbps</td>
<td>-</td>
<td>34mm</td>
<td>ECM1010</td>
</tr>
<tr>
<td>2</td>
<td>1</td>
<td></td>
<td>DB44 Female</td>
<td></td>
<td>921.6Kbps</td>
<td>-</td>
<td>34mm</td>
<td>ECM2010</td>
</tr>
</tbody>
</table>

## Multi I/O CardBus Interface

<table>
<thead>
<tr>
<th>Port</th>
<th>COM</th>
<th>LPT</th>
<th>COM</th>
<th>LPT</th>
<th>Serial Baud Rate</th>
<th>Power output</th>
<th>Bracket</th>
<th>Model NO.</th>
</tr>
</thead>
<tbody>
<tr>
<td>1</td>
<td>1</td>
<td></td>
<td>DB44 Female</td>
<td></td>
<td>921.6Kbps</td>
<td>-</td>
<td>54mm</td>
<td>CBM1010</td>
</tr>
<tr>
<td>2</td>
<td>1</td>
<td></td>
<td>DB44 Female</td>
<td></td>
<td>921.6Kbps</td>
<td>-</td>
<td>54mm</td>
<td>CBM2010</td>
</tr>
</tbody>
</table>
5.3 Contact Information

Customer satisfaction is our number one concern, and to ensure that customers receive the full benefit of our products, SUNIX services has been set up to provide technical support, driver updates, product information, and user’s manual updates.

The following services are provided

E-mail for technical support

................................................ info@sunix.com

World Wide Web (WWW) Site for product information:

................................................ http://www.sunix.com